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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/801,610	03/17/2004	Hisakatsu Yamaguchi	108390-00061 1910		
4372 ARENT FOX I	7590 04/04/200 PLLC	1	EXAMINER		
1050 CONNECTICUT AVENUE, N.W. SUITE 400 WASHINGTON, DC 20036			FILE, ERIN M		
			ART UNIT	PAPER NUMBER	
			2611		
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE		
3 MO	NTHS	04/04/2007	PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

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		Application No	р.	Applicant(s)			
Office Action Summary		10/801,610		YAMAGUCHI, HISAKATSU			
		Examiner		Art Unit			
	<u> </u>	Erin M. File		2611			
 Period for	The MAILING DATE of this communication a Reply	ppears on the cov	er sheet with the c	orrespondence a	ddress		
WHICH - Extens after S - If NO p - Failure Any re	PRTENED STATUTORY PERIOD FOR REF HEVER IS LONGER, FROM THE MAILING ions of time may be available under the provisions of 37 CFR IX (6) MONTHS from the mailing date of this communication. period for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by stated ply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS C 1.136(a). In no event, hor od will apply and will expir tute, cause the application	COMMUNICATION wever, may a reply be tim e SIX (6) MONTHS from to become ABANDONED	L. ely filed the mailing date of this () (35 U.S.C. § 133).	,		
Status							
1)⊠ F	Responsive to communication(s) filed on 17	' March 2004	-				
,		his action is non-fi	nal		•		
′=	Since this application is in condition for allow			secution as to th	e merits is		
•	closed in accordance with the practice unde	•	· •		o mono is		
Dispositio	on of Claims	•					
4) 🛛 (Claim(s) <u>1-16</u> is/are pending in the application	on.					
-	a) Of the above claim(s) is/are withd		eration.				
	Claim(s) is/are allowed.	,			·		
6)⊠ (Claim(s) <u>1-8</u> is/are rejected.						
7)🛛 (Claim(s) <u>9-16</u> is/are objected to.						
8) 🗌 (Claim(s) are subject to restriction and	d/or election requir	ement.				
Applicatio	on Papers						
9)□ т	he specification is objected to by the Exami	iner			•		
•			or b) objected to	by the Examine	er.		
10)⊠ The drawing(s) filed on <u>17 March 2004</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
	Replacement drawing sheet(s) including the corre				FR 1.121(d).		
	The oath or declaration is objected to by the				• •		
Priority ur	nder 35 U.S.C. § 119				•		
	cknowledgment is made of a claim for forei	an priority under 3	5115C & 110(a)	-(d) or (f)			
•	All b) Some * c) None of:	gii pilotity dilder o		-(d) or (i).			
,	I.⊠ Certified copies of the priority docume	ents have been rec	reived		·		
	2. Certified copies of the priority docume			on No.			
	B. Copies of the certified copies of the pr				l Stage		
`	application from the International Bure			a III allo i tadolia	. Gtago		
* Se	ee the attached detailed Office action for a li	•		d .			
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Attachment(s)						
	of References Cited (PTO-892)	4)	-				
	of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO/SB/08)	5) [Paper No(s)/Mail Da Notice of Informal Pa				
	No(s)/Mail Date <u>3/17/2004</u> .	6)	Other:	and the second			
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Application/Control Number: 10/801,610

Art Unit: 2611

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 2, 5, and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Weon (U.S. Patent No. 6,272,138).

Claims 1, 5, Weon discloses:

- clock generation circuit generating a clock signal (col. 2, lines 51-52, produces a first transmission clock);
- jitter supply circuit allowing the clock signal generated by the clock generation circuit to include jitter (col. 2, line 66-col. 3, line 1, outputs data stream including jitter according to first transmission clock); and
- a data transmission circuit transmitting data in sync with the clock signal including the jitter (col. 2, line 64-col. 3, line 1, provides synchronous timing to output data stream including jitter according to first transmission clock).
- Claims 2, 6, Weon further discloses the jitter supply circuit adjusts the amount of modulation and/or the frequency of the jitter included in the clock signal (jitter absorber 102 of fig. 1 reduces the frequency of the jitter).

Application/Control Number: 10/801,610 Page 3

Art Unit: 2611

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 3, 4, 7, and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Weon (U.S. Patent No. 6,272,138) as applied to claims 1, 2, 5, and 6 above, and further in view of Little et al. (U.S. Patent No. 6,640,194).

Claims 3, 4, 7, 8, although Weon fails to disclose the jitter supply circuit supplies, as the jitter, at least any one of a sinusoidal jitter and a random jitter, however, Little discloses the addition of sinusoidal jitter (col. 4, line 26). Because sinusoidal jitter is a type of deterministic jitter and is therefore easier to predict and model it have advantageous modeling and determination properties. Therefore it would have been obvious to one skilled in the art at the time of invention to incorporate the sinusoidal jitter as disclosed by Little into the invention of Weon.

Claim Objections

5. Claim 3 objected to because of the following informalities: the word *any* should be deleted in line 4. Appropriate correction is required.

Conclusion

Application/Control Number: 10/801,610 Page 4

Art Unit: 2611

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See Knapp et al. (U.S. Pub. No. 2004/0057542) in which the abstract discloses "The data pattern may be made up of a series of non-transitions which, regardless of any jitter in the data itself, does not impute any jitter onto a phase-locked loop triggered from an edge of the non-transitioning data pattern" which is related to the limitations of dependent claim 9.

Allowable Subject Matter

- 7. Claims 9-16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Erin M. File whose telephone number is 5712726040. The examiner can normally be reached on M-F 1-9:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Payne can be reached on 5712723024. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/801,610

Art Unit: 2611

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Page 5

Erin M. File

3/30/2007

DAVID C. PAYNE V SUPERVISORY PATENT EXAMINER